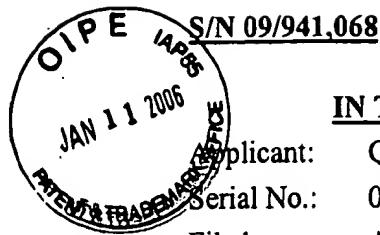


**EXPEDITED PROCEDURE – EXAMINING GROUP 2128**



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Qiyong Bian  
Serial No.: 09/941,068  
Filed: August 28, 2001  
Title: CO-SIMULATION OF VERILOG/PLI AND SYSTEM C MODULES USING  
REMOTE PROCEDURE CALL

Examiner: Thai Q. Phan

Group Art Unit: 2128

Docket No.: 303.741US1

**AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Final Office Action mailed October 19, 2005, please amend the application as follows:

Please enter  
this Amendment  
Thanks  
T.P.  
1/20/06